



深圳市一众显示科技有限公司

SHEN ZHEN TEAM SOURCE DISPLAY TECH. CO, LTD.

TFT-LCD Module Specification

Module NO.: TST043WQPO-01

Version: V2.0

☐ APPROVAL FOR SPECIFICATION

☐ APPROVAL FOR SAMPLE

For Customer' s Acceptance:	
Approved by	Comment

Team Source Display:		
Presented by	Reviewed by	Organized by

Version No.	Date	Content	Remark
V1.0	2015-12-25	Initial Release	

Contents

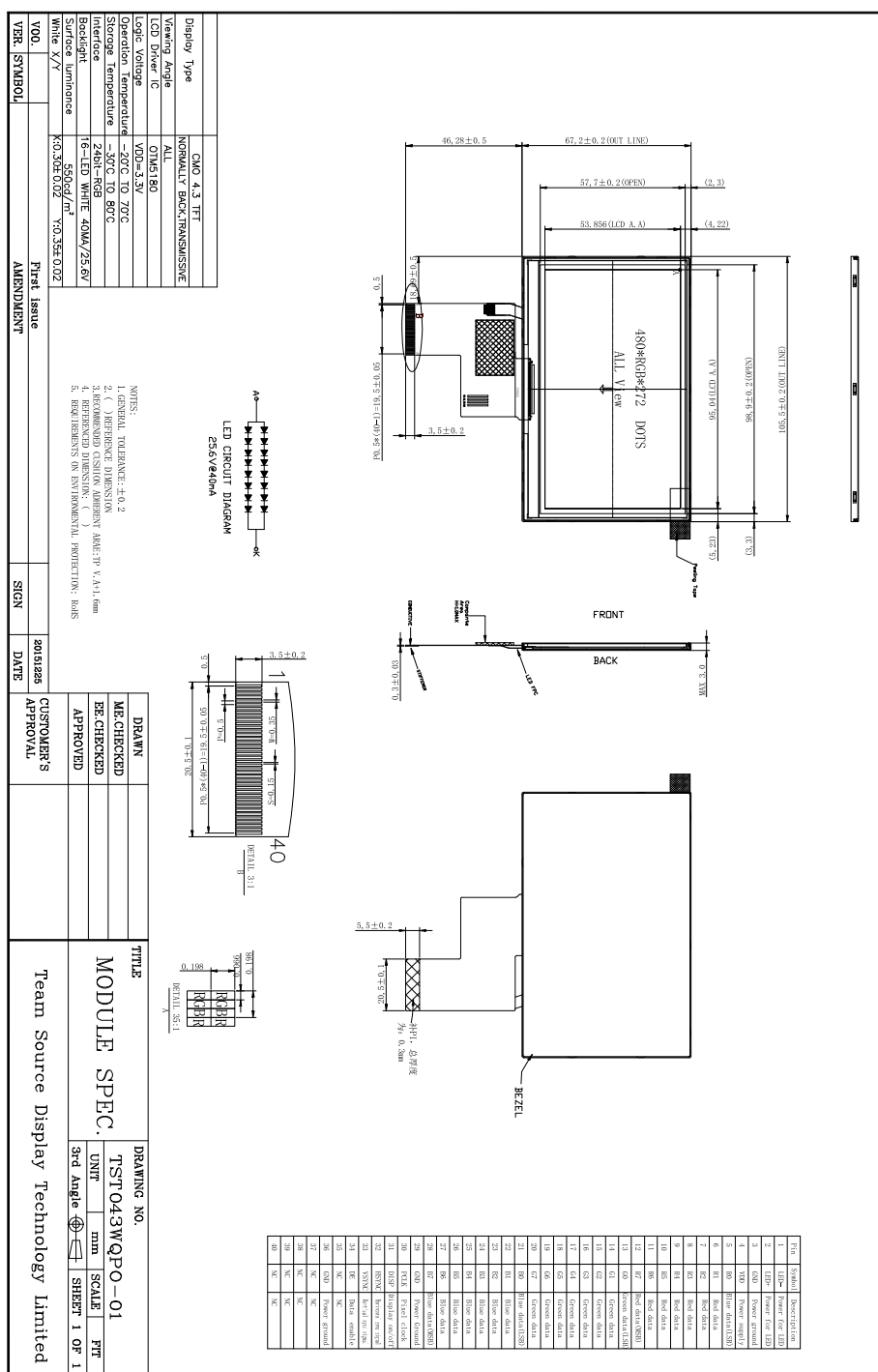
1. General Specification
2. Mechanical Drawing
3. Block Diagram
4. Interface Pin Function
5. Absolute Maximum Ratings
6. Electrical Characteristics
7. Optical Characteristics
8. Timing Characteristics
9. Standard Specification for Reliability
10. Specification of Quality Assurance
11. Packing Method
12. Handling Precaution



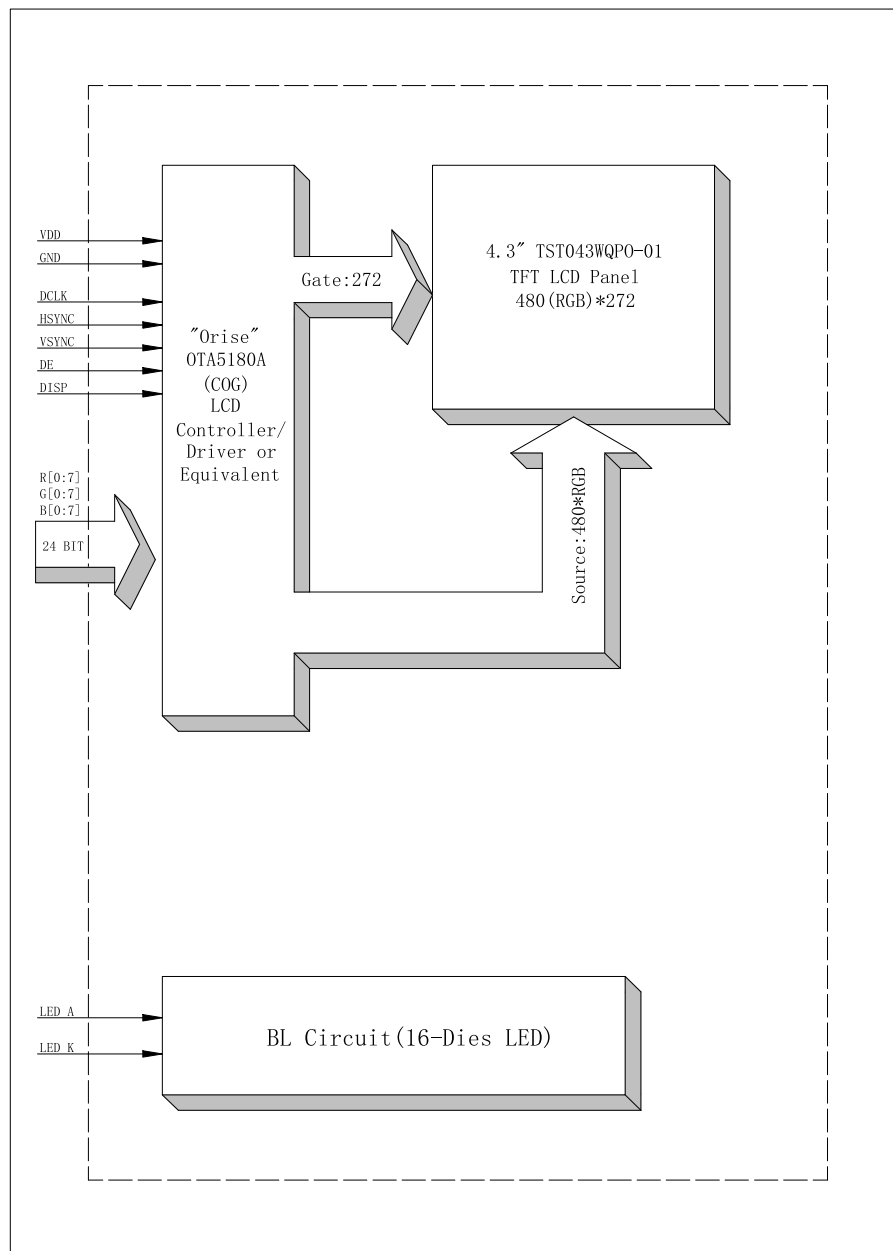
1. General Specification

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	105.5*67.2*3.0	MM
ACTIVE SIZE (W*H)	95.04*53.865	MM
PIXEL PITCH (W*H)	0.198*0.198	MM
NUMBER OF DOTS	480*272	
DIVER IC	OTA5180	
INTERFACE TYPE	24-BIT RGB	
TOP POLARIZER TYPE	ANTI-GLARE	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	-	O'CLOCK
COLORS	16.7M	
BACKLIGHT TYPE	16- CHIP WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

2. Mechanical Drawing



3. Block Diagram



4. Interface Pin Function

Pin No.	Symbol	Description
1	LEDK	Cathode of LED backlight
2	LEDA	Anode of LED backlight
3	GND	Power ground
4	VDD	Power supply
5~12	R0~R7	8-bit digital Red data input,
13~20	G0~G7	8-bit digital Green data input,
21~28	B0~B7	8-bit digital Blue data input,
29	GND	Power ground
30	PCLK	Clock signal; negative polarity
31	DISP	Display control / standby mode selection. DISP = "Low" : Standby; (Default) DISP = "High" : Normal display
32	HSYNC	Horizontal sync signal
33	VSYNC	Vertical sync signal
34	DEN	Data input enable. Active High to enable the data input.
35	NC	No connection.
36	GND	Power ground
37	NC	No connect
38	NC	No connect
39	NC	No connect
40	NC	No connect

5. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply voltage for analog	VDD	-0.3	4.5	V
Supply voltage for logic	VDD	-0.3	4.5	V
Supply current (One LED)	I _{LED}		30	mA
Operating temperature	T _{OP}	-20	+70	°C
Storage temperature	T _{ST}	-30	+80	°C

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times.

Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

6. Electrical Characteristics

6.1 Input Power

Item	Symbol	Min	Typ.	Max	Unit	Applicable terminal
Supply Voltage for Analog	VDD	3.0	3.3	3.6	V	
Supply Voltage for Logic	VDD	3.0	3.3	3.6	V	
Input Voltage	V _{IL}	GND	-	0.3VDD	V	
	V _{IH}	0.7 VDD	-	VDD		
Input leakage Current	I _{LKG}	-1		1	μA	

6.2 Backlight Driving Conditions

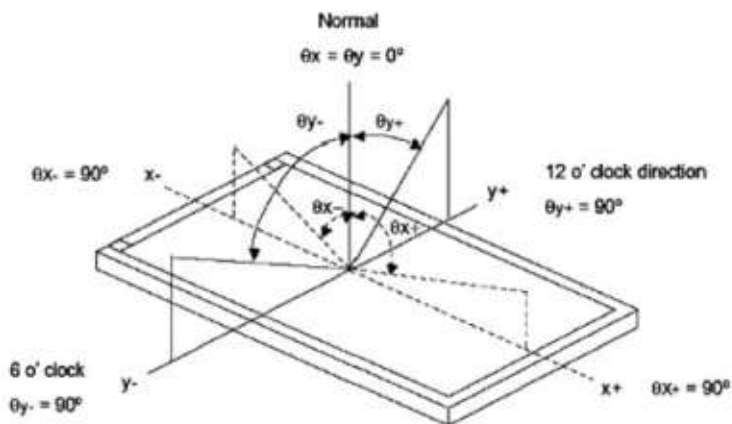
Item	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V _F	-	25.6	-	V	I _L =40mA
Current for LED Backlight	I _L		40		mA	
Power Consumption	P		1.024		W	
LED Life Time		30,000			Hr	Note

Note: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25°C

7. Optical Characteristics

ITEM		SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
				MIN	TYP.	MAX		
Luminance		L	$I_L = 40\text{mA}$	500	550	600	Cd/m^2	
Contrast Ratio		CR	$\theta = 0^\circ$	-	500	-		
Response Time		T_{ON}	25°C	-	35	45	ms	
		T_{OFF}						
CIE Color Coordinate	Red	X_R	Viewing normal angle	0.61	0.63	0.65		
		Y_R		0.33	0.35	0.37		
	Green	X_G		0.34	0.36	0.38		
		Y_G		0.57	0.59	0.61		
	Blue	X_B		0.13	0.15	0.17		
		Y_B		0.09	0.11	0.13		
	White	X_W		0.28	0.30	0.32		
		Y_W		0.33	0.35	0.37		
Viewing Angle	Hor.	θ_{X+}	$\text{CR} \geq 10$	80	85		Degree	
		θ_{X-}		80	85			
	Ver.	θ_{Y+}		70	85			
		θ_{Y-}		70	85			
Uniformity	Un			80			%	

Note 1: Definition of Viewing

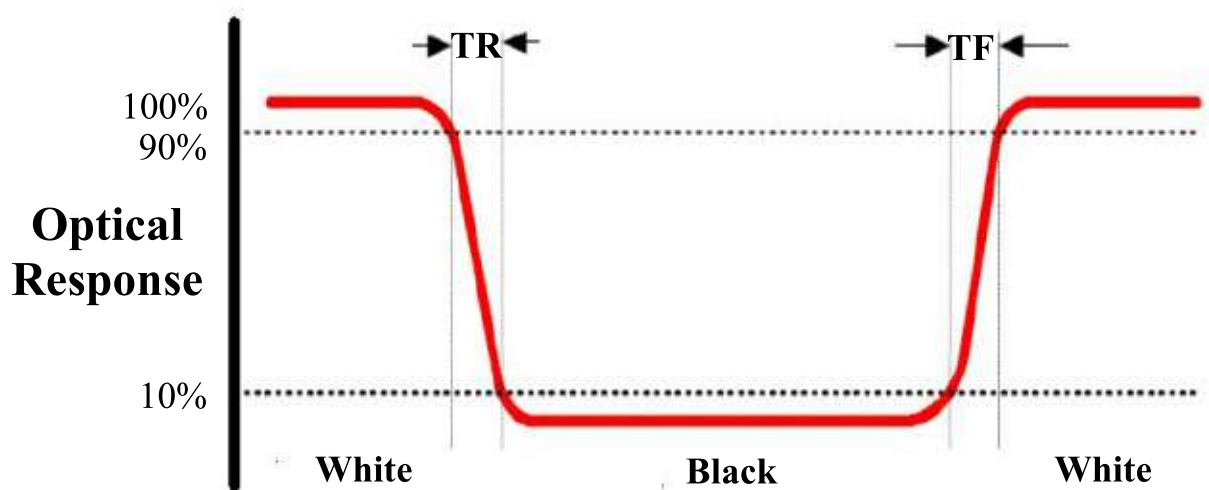


Angle θ_x and θ_y :

Note 2: Definition of contrast ratio CR:

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

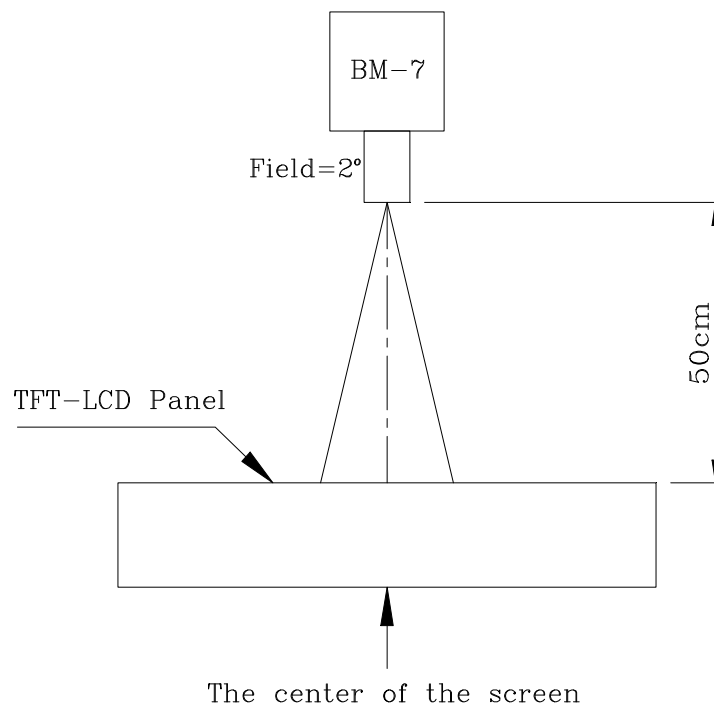
Note 3: Definition of Response Time(Tr,Tf)



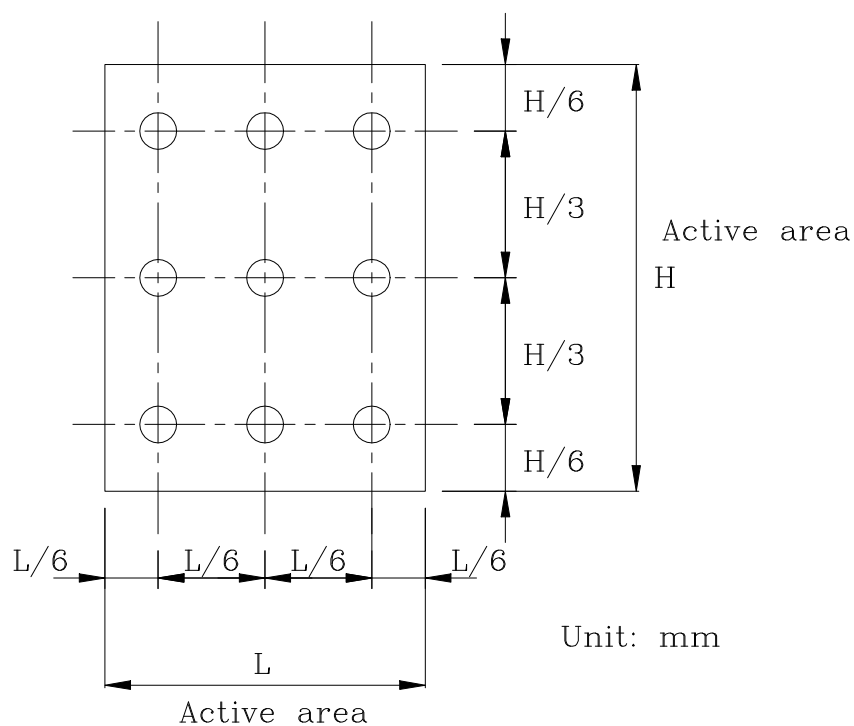
Note 4: Definition of Luminance

①The Brightness Test Equipment Setup

Field=2°(As measuring “black” image, field=2°is the best testing condition)

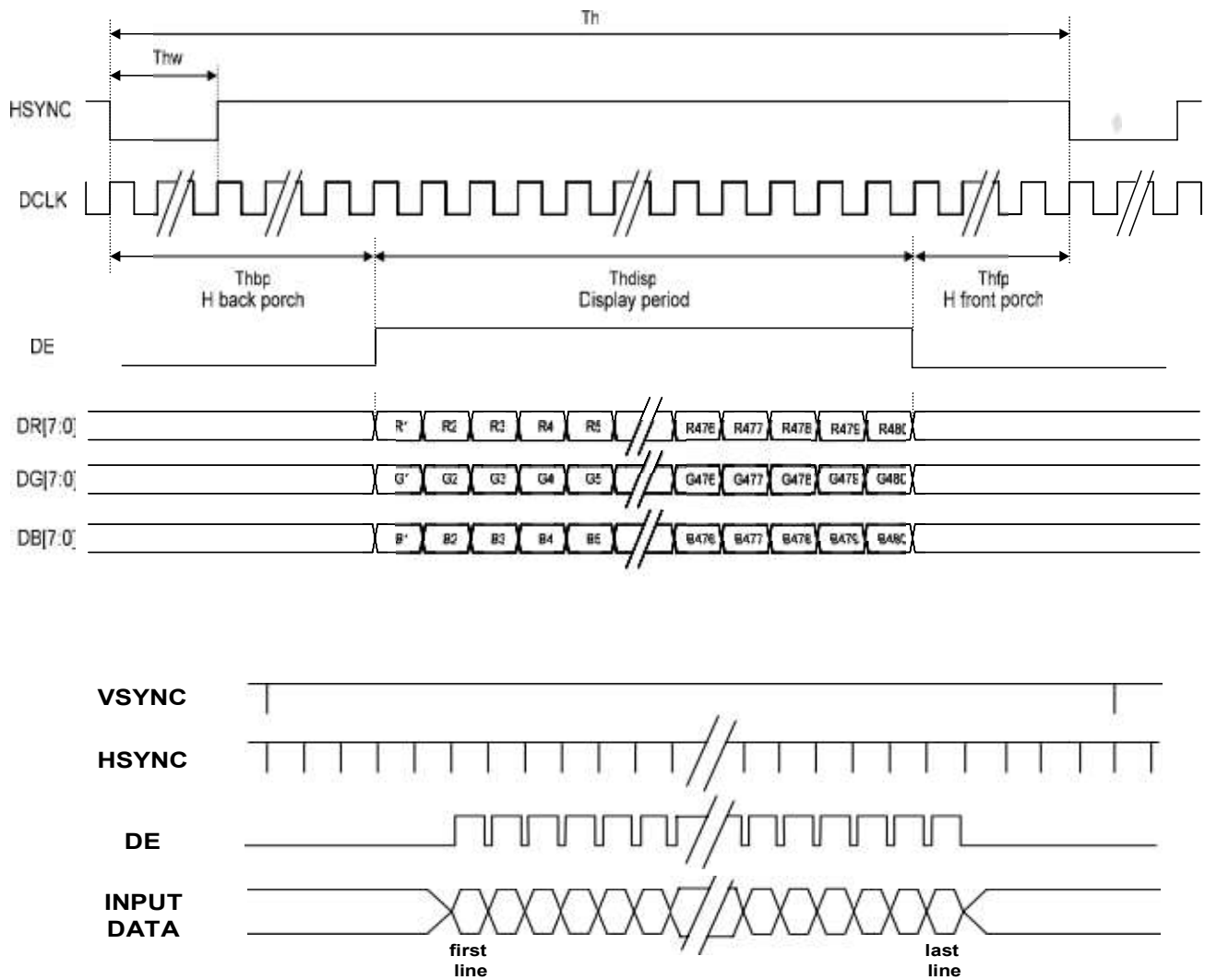


②The Brightness Test Point Setup

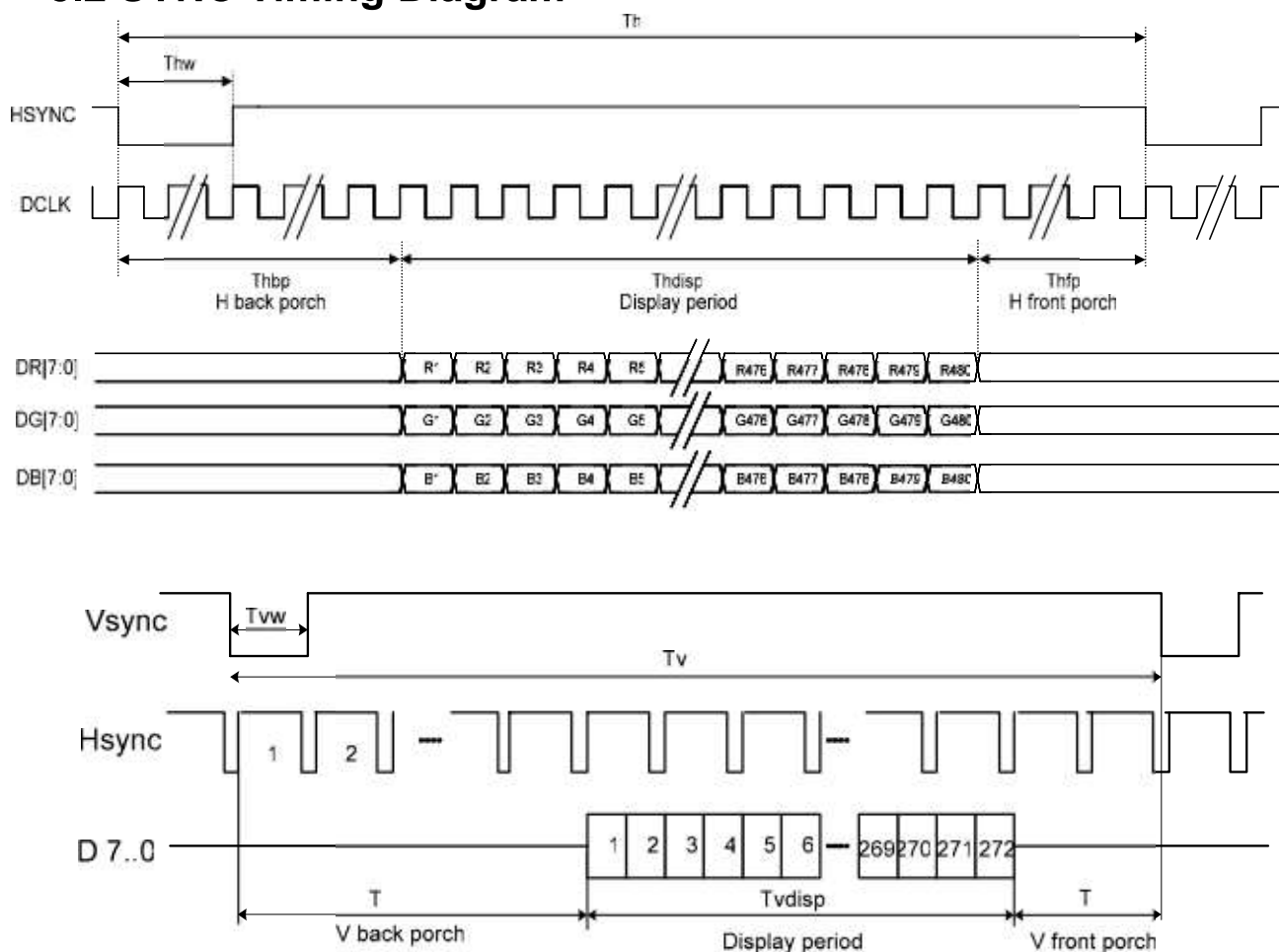


8. Timing Characteristics

8.1 SYNC-DE Mode Timing Diagram



8.2 SYNC Timing Diagram



8.3 Parallel Timing table

Item	Symbol	Min.	Typ.	Max.	Unit	
DCLK Frequency	Fclk	5	9	12	MHz	
DCLK Period	Tclk	83	110	200	ns	
Hsync	Period Time	T_h	490	531	605	DCLK
	Display Period	T_{hdisp}		480		DCLK
	Back Porch	T_{hbp}	8	43		DCLK
	Front Porch	T_{hfp}	2	8		DCLK
	Pulse Width	T_{hw}	1			DCLK
Vsync	Period Time	T_v	275	288	335	H
	Display Period	T_{vdisp}		272		H
	Back Porch	T_{vbp}	2	12		H
	Front Porch	T_{vfp}	1	4		H
	Pulse Width	T_{vw}	1	10		H

8.4 Power ON/OFF Sequence

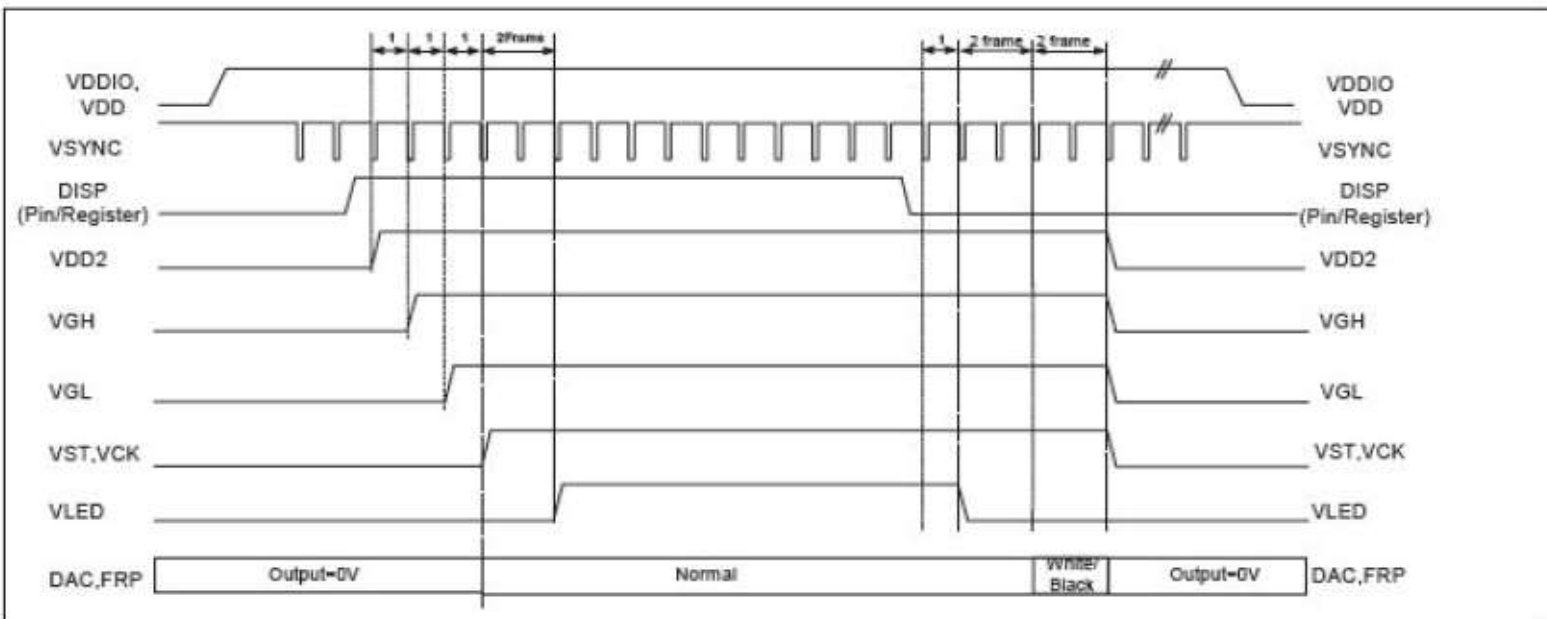


Fig. Power Sequence

Note:

- When MVA mode (Normally black LC) is applied, it needs to send **black pattern** to discharge the pixel.
- When TN_MODE2 or TN_MODE1 mode (Normally white LC) is applied, it needs to send **white pattern** to discharge the pixel.

9. Standard Specification for Reliability

9.1 Standard Specification for Reliability of LCD Module

No.	Item	Description
01	High temperature operation	The sample should be allowed to stand at 70℃ for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at -20℃ for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at 80℃ for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at -30℃ for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at 60℃,90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : -30℃ for 30 minutes → normal temperature for 5 minutes → +80℃ for 30 minutes → normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range : 10Hz ~ 55Hz Amplitude of vibration : 1.5mm Sweep time: 12 min X,Y,Z 2 hours for each direction.
08	Packing drop test	According to ASTM-D-5327.
09	Electrical Static Discharge	Air: ±4KV 150pF/330Ω 5 times
		Contact: ±2KV 150pF/330Ω 5 time

*Sample size for each test item is 3~5pcs

9.2 Testing Conditions and Inspection Criteria

For the final test, the testing sample must be stored at room temperature for 24 hours. After the tests listed in Table 9.2, standard specifications for reliability will be executed in order to ensure stability.

No .	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

9.3 MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($25\pm5^{\circ}\text{C}$), normal humidity ($50\pm10\%$ RH), and in area not exposed to direct sun light.
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10. Specification of Quality Assurance

10.1 Quality Test

Before delivering, the supplier should conduct the following tests to confirm the quality of products.

- Electrical-Optical Characteristics: According to the individual specification to test the product.
- Appearance Characteristics: According to the individual specification to test the product.
- Reliability Characteristics: According to the definition of reliability on the specification for testing products.

10.2 Delivery Test

Before delivering, the supplier should conduct the delivery test.

- Test method: According to MIL-STD105E.General Inspection Level II take a single Time.
- The defects classify of AQL as following:
Major defect: AQL = 0.65
Minor defect: AQL = 2.5
Total defects: AQL = 2.5

10.3 Non-conforming Analysis & Deal With Manners

10.3.1 Non-conforming Analysis

- Purchaser should provide the data detail of non-conforming sample and the non-conforming.
- After receiving the data detail from purchaser, the analysis of non-conforming should be finished within two weeks.
- If the analysis can't be finished on time, supplier must notice purchaser 3 days in advance.

10.3.2 Disposition of non-conforming

- If any product defect be found during assembling, supplier must change the good for every defect after confirmation.
- Both supplier and customer should analyze the reason and discuss the disposition of non-conforming when the reason of nonconforming is not sure.

10.4 Agreement items

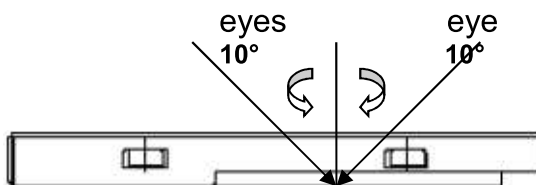
Both parties should negotiate together when the following problems happen.

- There is any problem of standard of quality assurance, and both sides should agree that it must be modified.
- There is any argument item which does not record in the standard of quality assurance.
- Any other special problem.

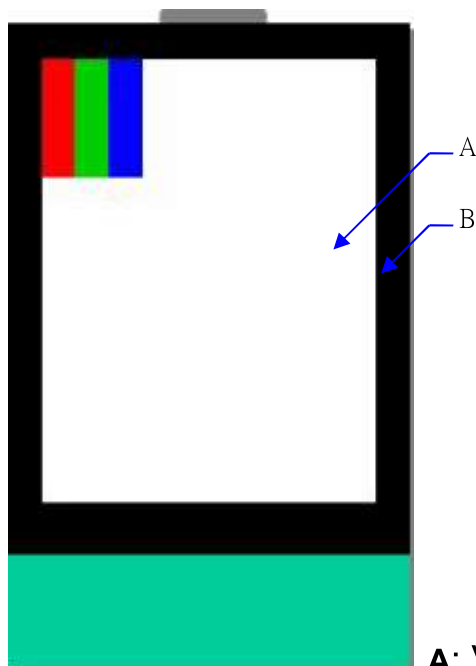
10.5 Standard of The Product Appearance Test

10.5.1 Manner of appearance test

- The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.
- When test the model of transmissive product must add the reflective plate.
- The test direction is base on around 10° of vertical line.
- Temperature: 25±5℃ Humidity: 60±10%RH



- Definition of area:

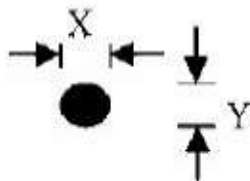
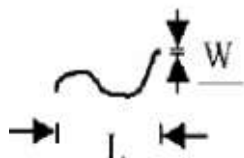


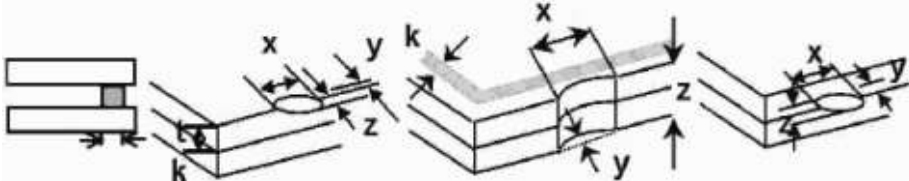
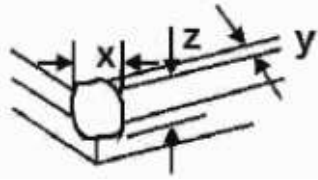
A: Viewing area B: Outside viewing area

10.5.2 Basic principle

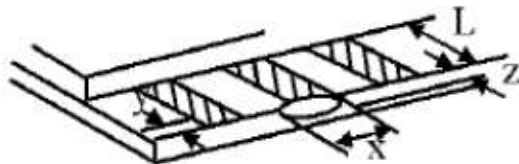
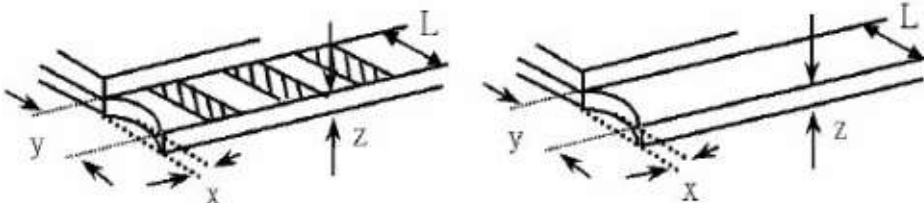
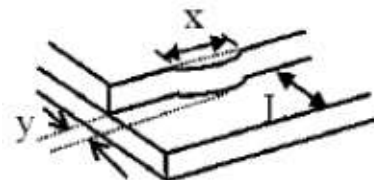
- When the standard can not be described, AQL will be applied.
- The sample of the lowest acceptable quality level must be negotiated by both supplier and customer when any dispute happened.
- New item must be added on time when it is necessary.

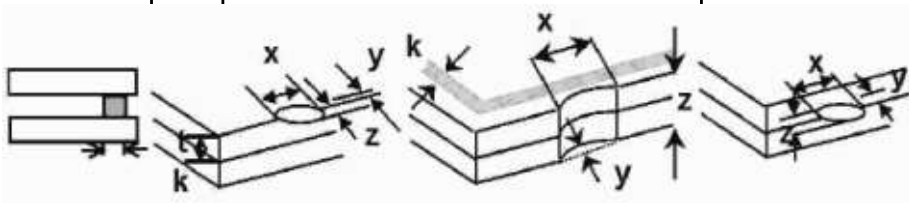
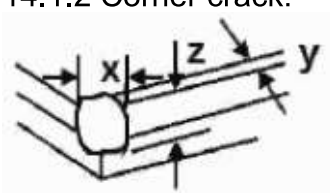
10.6 Inspection Specification

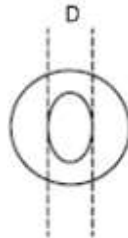
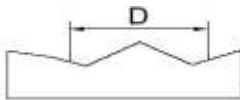
NO.	Item	Criterion	AQL														
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Flicker	0.65														
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	2.1 White and black or color spots on display $\leq 0.25\text{mm}$, no more than Five spots. 2.2 Densely spaced: No more than three spots within 3mm.	2.5														
03	LCD and Touch Panel black spots, white spots, contamination (non – display)	3.1 Round type: As following drawing $\Phi = (X+Y) / 2$ <div></div> <table><thead><tr><th>Size(mm)</th><th>Acceptable Q'ty</th></tr></thead><tbody><tr><td>$\Phi \leq 0.10$</td><td>Accept no dense</td></tr><tr><td>$0.10 < \Phi \leq 0.20$</td><td>2</td></tr><tr><td>$0.20 < \Phi \leq 0.25$</td><td>2</td></tr><tr><td>$0.25 < \Phi \leq 0.30$</td><td>1</td></tr><tr><td>$0.30 < \Phi$</td><td>0</td></tr></tbody></table> <p>* Densely spaced: No more than two spots within 3mm.</p>	Size(mm)	Acceptable Q'ty	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	2	$0.25 < \Phi \leq 0.30$	1	$0.30 < \Phi$	0	2.5		
		Size(mm)	Acceptable Q'ty														
$\Phi \leq 0.10$	Accept no dense																
$0.10 < \Phi \leq 0.20$	2																
$0.20 < \Phi \leq 0.25$	2																
$0.25 < \Phi \leq 0.30$	1																
$0.30 < \Phi$	0																
		3.2 Line type: (As following drawing) <div></div> <table><thead><tr><th>Length(mm)</th><th>Width(mm)</th><th>Acceptable Q'ty</th></tr></thead><tbody><tr><td>---</td><td>$W \leq 0.02$</td><td>Accept no dense</td></tr><tr><td>$L \leq 3.0$</td><td>$0.02 < W \leq 0.05$</td><td rowspan="2">2</td></tr><tr><td>$L \leq 2.5$</td><td>$0.03 < W \leq 0.08$</td></tr><tr><td>---</td><td>$0.08 < W$</td><td>Rejection</td></tr></tbody></table> <p>* Densely spaced: No more than two lines within 3mm.</p>	Length(mm)	Width(mm)	Acceptable Q'ty	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.05$	2	$L \leq 2.5$	$0.03 < W \leq 0.08$	---	$0.08 < W$	Rejection	2.5
Length(mm)	Width(mm)	Acceptable Q'ty															
---	$W \leq 0.02$	Accept no dense															
$L \leq 3.0$	$0.02 < W \leq 0.05$	2															
$L \leq 2.5$	$0.03 < W \leq 0.08$																
---	$0.08 < W$	Rejection															

NO.	Item	Criterion			AQL	
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction	Size Φ (mm)	Acceptable Q'ty	2.5	
			$\Phi \leq 0.20$	Accept no dense		
			$0.20 < \Phi \leq 0.50$	3		
			$0.50 < \Phi \leq 1.00$	2		
			$1.00 < \Phi$	0		
			Total Q'ty	3		
05	Scratches	Follow NO.3 -2 Line Type.				
06	Chipped glass	Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels:			2.5	
						
		z: Chip thickness	y: Chip width	x: Chip length		
		$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$		
		$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$		☉ Unit:
		mm				
		☉ If there are 2 or more chips, x is the total length of each chip				
		6.1.2 Corner crack:				
						
		z: Chip thickness	y: Chip width	x: Chip length		
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$				
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	☉ Unit:			
mm						
☉ If there are 2 or more chips, x is the total length of each chip						

NO.	Item	Criterion	AQL
08	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
09	Backlight elements	9.1 Illumination source flickers when lit. 9.2 Spots or scratches that appear when lit must be judged. Using LCD spot, lines and contamination standards. 9.3 Backlight doesn't light or color is wrong.	2.5 2.5 0.65
10	Bezel	Bezel must comply with product specifications.	2.5
11	PCB、COB	11.1 COB seal may not have pinholes larger than 0.2mm or contamination. 11.2 COB seal surface may not have pinholes through to the IC. 11.3 The height of the COB should not exceed the height indicated in the assembly diagram. 11.4 There may not be more than 2mm of sealant outside the seal area on PCB. And there should be no more than three places. 11.5 Parts on PCB must be the same as on the production characteristic chart, There should be no wrong parts, missing parts or excess parts. 11.6 The jumper on the PCB should conform to the product characteristic chart.	2.5 2.5 2.5 2.5 0.65 0.65
12	FPC	12.1 FPC terminal damage $\leq 1/2$ FPC terminal width and can not affect the function , we judge accept. 12.2 FPC alignment hole damage $\leq 1/2$ alignment area and can not affect the function , we judge accept.	2.5 2.5
13	Soldering	13.1 No cold solder joints, missing solder connections, oxidation or icicle. 13.2 No short circuits in components on PCB or FPC.	2.5 0.65

NO.	Item	Criterion	AQL																
07	Glass crack	<p>Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length</p> <p>7.2 Protrusion over terminal: 7.2.1 Chip on electrode pad:</p>  <table> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq 0.5\text{mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>7.2.2 Non-conductive portion:</p>  <table> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>⊙ If there chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙ If the product will be heat sealed by the customer, the alignment mark must not be damaged.</p> <p>7.2.3 Substrate protuberance and internal crack</p>  <table> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td>$y \leq 1/3L$</td> <td>$X \leq a$</td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$X \leq a$	2.5
		y: Chip width	x: Chip length	z: Chip thickness															
		$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$															
		y: Chip width	x: Chip length	z: Chip thickness															
$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$																	
y: width	x: length																		
$y \leq 1/3L$	$X \leq a$																		

NO.	Item	Criterion	AQL												
14	Touch Panel Chipped glass	<p>Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Touch Panel Total thickness a: LCD side length L: Electrode pad length</p> <p>14.1 General glass chip: 14.1.1 Chip on panel surface and crack between panels:</p>  <table> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$Z \leq t$</td> <td>$\leq 1/2 k$ and not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> </table> <p>⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip</p> <p>14.1.2 Corner crack:</p>  <table> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$z \leq t$</td> <td>$\leq 1/2 k$ and not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> </table> <p>⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$	2.5
z: Chip thickness	y: Chip width	x: Chip length													
$Z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$													
z: Chip thickness	y: Chip width	x: Chip length													
$z \leq t$	$\leq 1/2 k$ and not over viewing area	$x \leq 1/8a$													

NO.	Item	Criterion	AQL										
15	Touch Panel(Fish eye、dent and bubble on film)	<table><tr><th>SIZE(mm)</th><th>Acceptable Q'ty</th></tr><tr><td>$\Phi \leq 0.2$</td><td>Accept no dense</td></tr><tr><td>$0.2 < D \leq 0.4$</td><td>5</td></tr><tr><td>$0.4 < D \leq 0.5$</td><td>2</td></tr><tr><td>$0.5 < D$</td><td>0</td></tr></table> <div></div>	SIZE(mm)	Acceptable Q'ty	$\Phi \leq 0.2$	Accept no dense	$0.2 < D \leq 0.4$	5	$0.4 < D \leq 0.5$	2	$0.5 < D$	0	2.5
		SIZE(mm)	Acceptable Q'ty										
		$\Phi \leq 0.2$	Accept no dense										
		$0.2 < D \leq 0.4$	5										
		$0.4 < D \leq 0.5$	2										
$0.5 < D$	0												
16	Touch Panel Newton ring	Newton ring dimension $\leq 1/2$ touch panel area and not affect font and line distortion($\leq 2.5\%$) , it is acceptable.	2.5										
17	Touch Panel Linearity	Less than 2.5% is acceptable.	2.5										
18	LCD Ripple	Touch the touch panel , can not see the LCD ripple. Pen: R 1.0mm silicon rubber. Operation Force: 80g	2.5										
19	General appearance	19.1 Pin type must match type in specification sheet.	0.65										
		19.2 LCD pin loose or missing pins.	0.65										
		19.3 Product packaging must the same as specified on packaging specification sheet.	0.65										
		19.4 Product dimension and structure must conform to product specification sheet.	0.65										

11. Packing Method

PARAMETER	Specification	Unit
Outside box	390(L) x 350(W) x 480(H)	mm
Inside pearl wool box	375(L) x 340(W) x 100(H)	mm
Product quantity of Inside box	36	pcs
Total product quantity	36*4=144	pcs
Total weight	8.8 \pm 0.5	Kg

12. Handling Precautions

12.1 Mounting method

A panel of LCD module made by our company consists of two thin glass plates with polarizers that easily get damaged. When doing the mounting of the LCD module, extreme care should be used when handling the LCD modules.

12.2 Cautions of LCD handling and cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketene
- Aromatics

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers. So we recommend you:

Connect any unused input terminal to V_{dd} or V_{ss} . Do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

12.4 Packaging

- Module employs LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

12.5 Caution for operation

-It is an indispensable condition to drive LCD module within the limits of the specified voltage since the higher voltage over the limits may cause the shorter life of LCD module.

-An electrochemical reaction due to DC (direct current) causes LCD undesirable deterioration so that the uses of DC (direct current) drive should be avoided.

-Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD module may show dark color in them. However those phenomena do not mean malfunction or out of order of LCD module, which will come back in the specified operating temperature.

12.6 Storage

In the case of storing for a long period of time, the following ways are recommended:

- Storage in polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with not desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping the storage temperature range.
- Storing with no touch on polarizer surface by any thing else.

12.7 Safety

-It is recommendable to crash damaged or unnecessary LCD into pieces and to wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

-When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well at once with soap and water.

